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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent Application

Inventor(s): **Craig Nemecek**

Application No.: **09/975,105**

Group Art Unit:

Filed: **10/10/01**

Examiner:

Title: **HOST TO FPGA INTERFACE IN AN IN-CIRCUIT EMULATION SYSTEM**

Form 1449

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<b>TP</b>	<b>A</b>	<b>6,460,172</b>	<b>10/01/02</b>	<b>Insenser Farre et al.</b>	<b>716</b>	<b>17</b>	<b>06/21/00</b>

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	<b>B</b>							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	<b>C</b>	
Examiner <b>Thauphan</b>	Date Considered <b>1/17/06</b>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.